IN THE SPECIFICATION

Please amend paragraph [022] of the specification as follows:

[022] Figure 3 shows a flow chart 300 illustrating some embodiments of video filter 203. Flow chart 300 describes an algorithm executed in on a processor for processing received video data. The algorithm represented by flow chart 300 can be implemented by software stored in a memory (not shown) and executed on a processor (not shown), may be implemented in digital hardware in a digital signal processor, or may be implemented in a combination of digital circuitry and software executed on the processor.